

REMARKS

Pending Claims:

In this application, claims 1, 3, 4, 6, 7, 9, 11-13, 15, 16, 20-26, 28, 29, 31, 32, 34, 36, 37, 39, 40, 42, 43, 45, and 47-58 are currently pending. Claims 3, 4, 6, 7, 9, 28, 29, 31, 34, 39, 40, 42, and 45 have not be altered since filing. Claims 1, 11-13, 15, 16, 20-26, 32, 36, 37, 43, and 47 are amended by this Response. Claims 2, 5, 8, 10, 14, 17-19, 27, 30, 33, 35, 38, 41, 44, and 46 have been cancelled in the Response. Claims 48-58 have been added. Entry of these amendments is respectfully requested.

Claim Objections--General:

The office action objected to claims 32, 43, and 47 due to the lack of the word "volatile" in the phrase "shared memory" in claims 32 and 43, and the lack of the phrase "dual port" in the phrase "static random access memory" in claim 47. The above amendments correct these informalities.

Claim Objections—35 USC § 112:

The office action has rejected claims 38 and 41 under 35 USC § 112. These claims have been cancelled, thereby eliminating these rejections.

Rejection under 35 U.S.C. §103

The office action rejected many of the claims as obvious over Reueter (U.S. Patent No. 5,754,863) in view of Von Ahnen (U.S. Patent No. 6,400,717) and Klein (U.S. Patent No. 6,216,224). These rejected claims included the original independent claims 1, 13, 26, and 37. The office action noted, however, that claims 10, 22, 35, and 46 would be allowable if rewritten in independent form, noting that the prior art fails to disclose or suggest synchronous static random access memory. The office action also noted that claims 7, 19, 32, and 43 would be allowable if rewritten in independent form, noting that the prior art fails to disclose or suggest a system or method where "the first processor provides identity to each of the plurality of processors by posting information through volatile memory."

In response, the Applicant has amended each of the original independent claims to include limitations found in one of the otherwise allowable dependent claim. Claims 1, 26, and 37 have been amended to include the limitations of claim 10, 35, and 46,

respectively, thereby incorporating limitations related to the use of synchronous static random access memory. Claim 13 has been amended to include the limitations of claims 18 and 19, thereby incorporating a limitation relating to the provision of identity from a master processor to a plurality of slave processors by posting information through volatile memory. Other claim amendments have been made to clarify language, or to make the claim language consistent with the Specification.

Since all of the original independent claims have now been amended to include limitations from dependent claims held to be allowable in the office action, the Applicant respectfully requests that the remaining independent claims are in condition for allowance.

New Claims:

Claims 48-58 are new claims, and include independent claims 48, 49, 50, 52, and 58. Claims 48, 49, 52, and 58 all include limitations relating to synchronous memory, which is not taught or suggested in the prior art. Claim 50 includes a limitation relating to the provision of identity to two slave processors through volatile memory devices. This also is not found in the prior art. These claims are supported by the Specification as originally filed.

CONCLUSION

All of the claims remaining in this application should now be seen to be in condition for allowance. The prompt issuance of a notice to that effect is solicited.

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